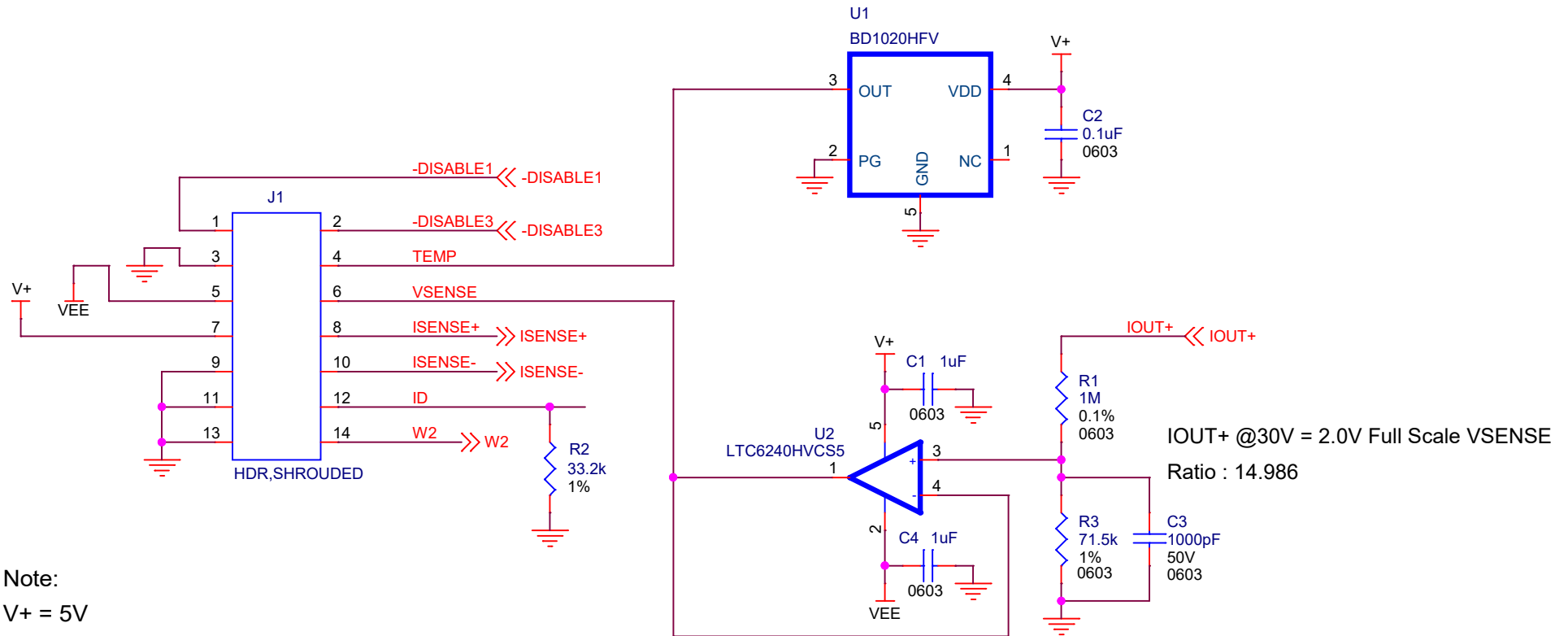




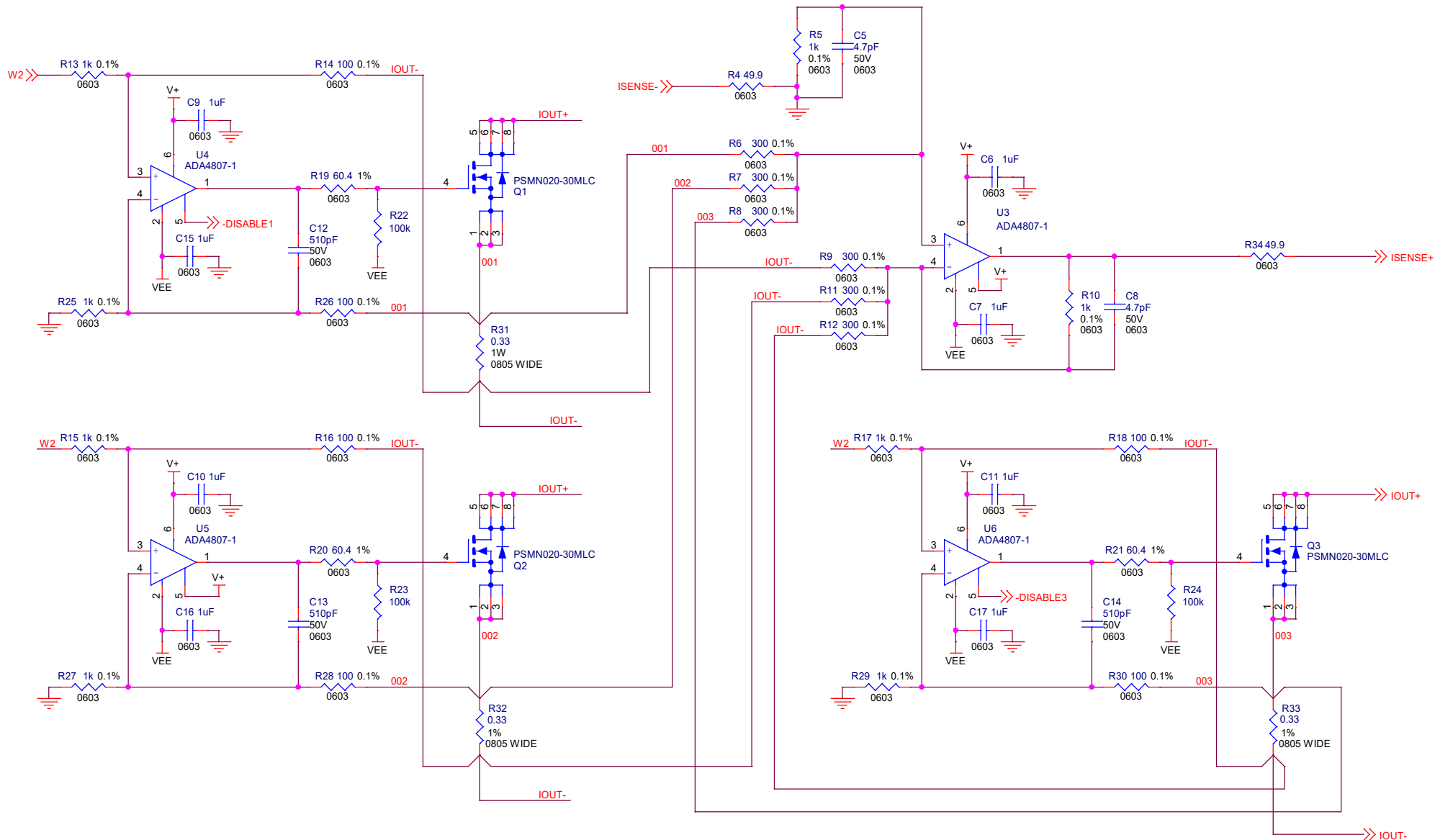
ADDED TO PARTS LIST	
LB1	LABEL
PCB1	PCB, LB3058A      REV03
STNCL	TOOL, STENCIL, LB3058A   REV03

REVISION HISTORY				
ECO	REV	DESCRIPTION	DATE	APPROVED
	3	PROTO	09/17/20	<i>BL</i>



Note:  
V+ = 5V  
Vee = -2.5V  
-DISABLEX = 0V - 3.3V

<b>CUSTOMER NOTICE</b> ANALOG DEVICES HAS MADE A BEST EFFORT TO DESIGN A CIRCUIT THAT MEETS CUSTOMER-SUPPLIED SPECIFICATIONS; HOWEVER, IT REMAINS THE CUSTOMER'S RESPONSIBILITY TO VERIFY PROPER AND RELIABLE OPERATION IN THE ACTUAL APPLICATION. COMPONENT SUBSTITUTION AND PRINTED CIRCUIT BOARD LAYOUT MAY SIGNIFICANTLY AFFECT CIRCUIT PERFORMANCE OR RELIABILITY. CONTACT ANALOG DEVICES APPLICATIONS ENGINEERING FOR ASSISTANCE.	<b>APPROVALS</b>		  www.analog.com	
	PCB DES.	<i>MF</i>	<b>TITLE: SCHEMATIC</b> 1A LTpowerAnalyzer Current Probe	
	APP ENG.	<i>BL</i>		
THIS CIRCUIT IS PROPRIETARY TO ANALOG DEVICES AND SUPPLIED FOR USE WITH ANALOG DEVICES PARTS.			IC NO. LTC6240HVCS5	<b>SCHEMATIC NO. AND REVISION:</b> 710-LB3058A-A_REV03
			SKU NO. LB3058A-A	
		SIZE: NONE	DATE: Thursday, September 17, 2020	SHEET 1 OF 2



NOTE:  
 C5 & C8 COMPENSATE FOR RSENSE PARASITIC  
 INDUCTANCE = 1.5nH (333m and 50m) or 1.0nH (22m & 10m)  
 $C5 \text{ \& \& C8} = LSENSE / (R5 * RSENSE)$   
 CURRENT MONITOR =  $(V(ISENSE+) - V(ISENSE-)) * 3 / RSENSE / 10$   
 RSENSE VOLTAGE =  $V(W2) / 10$   
 RSENSE VOLTAGE DESIGN MAXIMUM = 200mV  
 R2 ID RESISTOR = ( TOTAL RSENSE ) \* 100k

OPTIONS							
Version	Imin	Imax	Rsense	C5&C8	R19-R21	C12-C14	R2 Q1-Q3
1A	10mA	1.2A	0.33	4.7pF	60.4	510pF	33.2k PSMN020-30MLC

## CUSTOMER NOTICE

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THIS CIRCUIT IS PROPRIETARY TO ANALOG DEVICES AND SUPPLIED FOR USE WITH ANALOG DEVICES PARTS.

## APPROVALS

PCB DES.	<i>MS</i>
APP ENG.	<i>BL</i>



www.analog.com

TITLE: SCHEMATIC

1A LTpowerAnalyzer Current Probe

IC NO. LTC6240HVC55

SKU NO. LB3058A-A

SCHEMATIC NO. AND REVISION:

710-LB3058A-A\_REV03

SIZE: NONE

DATE: Thursday, September 17, 2020

SHEET 2 OF 2